Report Test 1

General Information

Model Name: PUC1200W Customer: Serial No: 0000000001
Order No.: 01 Lot No.: Total Load No.: 11
Environment: 25 Inspector: root
YYYY_MM_DD: 2008/01/14 Begin Time: AM 09:26:44 End Time: AM 09:27:27

SEQ.1: Set Up Function () ---------- PASS

Vin_Port (1-4) = 1 Vin type = AC
ON Phase (mS) = 0.000 OFF Phase = 0.000
Setup off(Sec) = 0.100 PAUSE function= N
Display Message= PLEASE PRESS

Relay status: Pre value-1 = 01 Pre value-2 = 01
TTL status : Pre value-1 = 0000 Pre value-2 = 0000
Change state delay: For relay = 0.800 For TTL = 0.000
Ext. device GPIB address = * GPIB EOS byte = 1
Message for ext. device = V10

	Load Name	MODE	Ifs	Vfs	Meas	BITS
1.	5V	I	50	10	UUT	0000
2.	3.3V	I	50	10	UUT	0000
3.	12V3	I	50	100	UUT	0000
4.	12V3	I	50	100	UUT	0000
5.	12V2	I	50	100	UUT	0000
6.	12V2	I	50	100	UUT	0000
7.	12V1	I	50	100	UUT	0000
8.	12V1	I	50	100	UUT	0000
9.	12V4	I	50	100	UUT	0000
10.	12V4	I	50	100	UUT	0000
11.	5VSB	I	50	10	UUT	0000

SEQ.2: Input/Output Test (20% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 2.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	2.603	Iinpk+	*	*	4.186
Pin	*	*	290.062	Iinpk-			4.173
Pdc	*	*	238.772	Pf	*	*	0.988
Eff	*	80.000	82.318	Vin	264.000	90.000	112.775

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	4.280	*	*	4.278	*	0.004

2.	0.010	4.570	*	*	4.556	*	0.003
3.	0.010	5.140	*	*	5.140	*	0.006
4.	0.010	0.000	*	*	0.003	*	0.004
5.	0.010	2.860	*	*	2.850	*	0.006
6.	0.010	0.000	*	*	0.000	*	0.003
7.	0.010	3.040	*	*	3.038	*	0.006
8.	0.010	0.000	*	*	0.000	*	0.006
9.	0.010	5.480	*	*	5.506	*	0.010
10.	0.010	0.000	*	*	0.000	*	0.008
11.	0.010	0.680	*	*	0.678	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.108	0.050	*	0.024
2.	3.465	3.135	3.313	0.120	*	0.022
3.	12.600	11.400	12.000	0.120	*	0.000
4.	12.600	11.400	11.956	0.120	*	0.075
5.	12.600	11.400	11.956	0.120	*	0.075
6.	12.600	11.400	11.962	0.120	*	0.050
7.	12.600	11.400	11.993	0.120	*	0.050
8.	12.600	11.400	11.981	0.120	*	0.101
9.	12.600	11.400	12.018	0.120	*	0.075
10.	12.600	11.400	12.025	0.120	*	0.046
11.	5.250	4.750	5.062	0.050	*	0.030

SEQ.3: Input/Output Test (30% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	3.778	Iinpk+	*	*	6.012
Pin	*	*	422.375	Iinpk-			6.002
Pdc	*	*	357.587	Pf	*	*	0.994
Eff	*	80.000	84.661	Vin	264.000	90.000	112.400

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	6.420	*	*	6.412	*	0.004
2.	0.010	6.850	*	*	6.840	*	0.003
3.	0.010	3.710	*	*	3.696	*	0.005
4.	0.010	4.000	*	*	4.003	*	0.004
5.	0.010	4.280	*	*	4.271	*	0.005
6.	0.010	0.000	*	*	0.000	*	0.004
7.	0.010	4.570	*	*	4.566	*	0.006
8.	0.010	0.000	*	*	0.000	*	0.007
9.	0.010	8.220	*	*	8.257	*	0.010
10.	0.010	0.000	*	*	0.000	*	0.008
11.	0.010	1.030	*	*	1.028	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read

1.	5.250	4.750	5.102	0.050	*	0.029
2.	3.465	3.135	3.306	0.120	*	0.025
3.	12.600	11.400	11.987	0.120	*	0.025
4.	12.600	11.400	11.950	0.120	*	0.050
5.	12.600	11.400	11.943	0.120	*	0.050
6.	12.600	11.400	11.950	0.120	*	0.075
7.	12.600	11.400	11.981	0.120	*	0.100
8.	12.600	11.400	11.968	0.120	*	0.075
9.	12.600	11.400	12.018	0.120	*	0.075
10.	12.600	11.400	12.025	0.120	*	0.052
11.	5.250	4.750	5.036	0.050	*	0.030

SEQ.4: Input/Output Test (40% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	4.989	Iinpk+	*	*	7.695
Pin	*	*	557.500	Iinpk-			7.752
Pdc	*	*	475.903	Pf	*	*	0.997
Eff	*	80.000	85.364	Vin	264.000	90.000	112.012

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	8.570	*	*	8.565	*	0.004
2.	0.010	9.130	*	*	9.118	*	0.004
3.	0.010	5.280	*	*	5.275	*	0.005
4.	0.010	5.000	*	*	5.006	*	0.003
5.	0.010	5.710	*	*	5.696	*	0.005
6.	0.010	0.000	*	*	0.000	*	0.004
7.	0.010	6.090	*	*	6.088	*	0.006
8.	0.010	0.000	*	*	0.000	*	0.008
9.	0.010	5.960	*	*	5.986	*	0.010
10.	0.010	5.000	*	*	4.990	*	0.008
11.	0.010	1.370	*	*	1.365	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.095	0.050	*	0.030
2.	3.465	3.135	3.298	0.120	*	0.027
3.	12.600	11.400	11.975	0.120	*	0.000
4.	12.600	11.400	11.937	0.120	*	0.050
5.	12.600	11.400	11.937	0.120	*	0.050
6.	12.600	11.400	11.937	0.120	*	0.050
7.	12.600	11.400	11.981	0.120	*	0.075
8.	12.600	11.400	11.968	0.120	*	0.100
9.	12.600	11.400	11.975	0.120	*	0.075
10.	12.600	11.400	11.987	0.120	*	0.057
11.	5.250	4.750	5.011	0.050	*	0.033

SEQ.5: Input/Output Test (50% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	6.233	Iinpk+	*	*	9.557
Pin	*	*	695.375	Iinpk-			9.602
Pdc	*	*	594.357	Pf	*	*	0.998
Eff	*	80.000	85.473	Vin	264.000	90.000	111.687

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	10.710	*	*	10.700	*	0.004
2.	0.010	11.420	*	*	11.409	*	0.004
3.	0.010	6.850	*	*	6.856	*	0.006
4.	0.010	6.000	*	*	5.996	*	0.004
5.	0.010	3.140	*	*	3.137	*	0.006
6.	0.010	4.000	*	*	3.995	*	0.004
7.	0.010	7.610	*	*	7.607	*	0.007
8.	0.010	0.000	*	*	0.000	*	0.008
9.	0.010	6.700	*	*	6.731	*	0.011
10.	0.010	7.000	*	*	6.993	*	0.009
11.	0.010	1.710	*	*	1.703	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.087	0.050	*	0.031
2.	3.465	3.135	3.290	0.120	*	0.032
3.	12.600	11.400	11.968	0.120	*	0.025
4.	12.600	11.400	11.925	0.120	*	0.075
5.	12.600	11.400	11.925	0.120	*	0.050
6.	12.600	11.400	11.925	0.120	*	0.050
7.	12.600	11.400	11.975	0.120	*	0.075
8.	12.600	11.400	11.962	0.120	*	0.075
9.	12.600	11.400	11.962	0.120	*	0.075
10.	12.600	11.400	11.968	0.120	*	0.061
11.	5.250	4.750	4.985	0.050	*	0.033

SEQ.6: Input/Output Test (60% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000

7.	12V1	I	50	100	UUT	0000	300	20M	8.000	
8.	12V1	I	50	100	UUT	0000	300	20M	8.000	
9.	12V4	I	50	100	UUT	0000	300	20M	8.000	
10.	12V4	I	50	100	UUT	0000	300	20M	8.000	
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000	

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	7.506	Iinpk+	*	*	11.455
Pin	*	*	835.000	Iinpk-			11.520
Pdc	*	*	712.819	Pf	*	*	0.999
Eff	*	80.000	85.368	Vin	264.000	90.000	111.350

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	12.850	*	*	12.846	*	0.005
2.	0.010	13.700	*	*	13.696	*	0.005
3.	0.010	7.420	*	*	7.421	*	0.006
4.	0.010	8.000	*	*	8.003	*	0.004
5.	0.010	4.570	*	*	4.559	*	0.006
6.	0.010	4.000	*	*	3.993	*	0.005
7.	0.010	4.130	*	*	4.126	*	0.008
8.	0.010	5.000	*	*	5.000	*	0.008
9.	0.010	8.440	*	*	8.477	*	0.011
10.	0.010	8.000	*	*	7.987	*	0.010
11.	0.010	2.050	*	*	2.053	*	0.001

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.080	0.050	*	0.034
2.	3.465	3.135	3.281	0.120	*	0.037
3.	12.600	11.400	11.956	0.120	*	0.025
4.	12.600	11.400	11.912	0.120	*	0.050
5.	12.600	11.400	11.912	0.120	*	0.050
6.	12.600	11.400	11.918	0.120	*	0.075
7.	12.600	11.400	11.950	0.120	*	0.075
8.	12.600	11.400	11.937	0.120	*	0.075
9.	12.600	11.400	11.993	0.120	*	0.075
10.	12.600	11.400	12.000	0.120	*	0.068
11.	5.250	4.750	4.958	0.050	*	0.043

SEQ.7: Input/Output Test (70% LOAD) ---------- PASS

Vin = 115.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	8.676	Iinpk+	*	*	13.175
Pin	*	*	963.250	Iinpk-			13.250
Pdc	*	*	819.058	Pf	*	*	1.000
Eff	*	80.000	85.031	Vin	264.000	90.000	111.012

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	14.990	*	*	14.984	*	0.005
2.	0.010	15.980	*	*	15.968	*	0.006
3.	0.010	8.990	*	*	9.003	*	0.006
4.	0.010	8.000	*	*	8.003	*	0.005
5.	0.010	4.990	*	*	4.990	*	0.006
6.	0.010	5.000	*	*	4.993	*	0.005
7.	0.010	5.650	*	*	5.650	*	0.009
8.	0.010	5.000	*	*	5.001	*	0.010
9.	0.010	9.180	*	*	9.221	*	0.013
10.	0.010	10.000	*	*	9.990	*	0.011
11.	0.010	2.400	*	*	2.406	*	0.002

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.072	0.050	*	0.037
2.	3.465	3.135	3.274	0.120	*	0.040
3.	12.600	11.400	11.943	0.120	*	0.050
4.	12.600	11.400	11.906	0.120	*	0.075
5.	12.600	11.400	11.906	0.120	*	0.050
6.	12.600	11.400	11.906	0.120	*	0.075
7.	12.600	11.400	11.943	0.120	*	0.100
8.	12.600	11.400	11.931	0.120	*	0.100
9.	12.600	11.400	11.975	0.120	*	0.100
10.	12.600	11.400	11.981	0.120	*	0.072
11.	5.250	4.750	4.932	0.050	*	0.042

SEQ.8: Input/Output Test (80% LOAD) ---------- PASS

Vin = 116.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	10.042	Iinpk+	*	*	15.145
Pin	*	*	1121.250	Iinpk-			15.295
Pdc	*	*	948.518	Pf	*	*	1.000
Eff	*	80.000	84.595	Vin	264.000	90.000	111.625

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	17.130	*	*	17.118	*	0.006
2.	0.010	18.260	*	*	18.246	*	0.006
3.	0.010	10.560	*	*	10.559	*	0.006
4.	0.010	10.000	*	*	10.003	*	0.005
5.	0.010	5.420	*	*	5.409	*	0.007
6.	0.010	6.000	*	*	5.997	*	0.006
7.	0.010	6.180	*	*	6.175	*	0.010
8.	0.010	6.000	*	*	6.002	*	0.010
9.	0.010	10.920	*	*	10.970	*	0.013
10.	0.010	11.000	*	*	10.990	*	0.011
11.	0.010	2.740	*	*	2.740	*	0.002

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.063	0.050	*	0.043
2.	3.465	3.135	3.265	0.120	*	0.045
3.	12.600	11.400	11.931	0.120	*	0.025
4.	12.600	11.400	11.893	0.120	*	0.075
5.	12.600	11.400	11.893	0.120	*	0.050
6.	12.600	11.400	11.893	0.120	*	0.075
7.	12.600	11.400	11.931	0.120	*	0.100
8.	12.600	11.400	11.918	0.120	*	0.101
9.	12.600	11.400	11.975	0.120	*	0.100
10.	12.600	11.400	11.981	0.120	*	0.081
11.	5.250	4.750	4.906	0.050	*	0.036

SEQ.9: Input/Output Test (90% LOAD) ---------- PASS

Vin = 117.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

	Load Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	11.286	Iinpk+	*	*	17.160
Pin	*	*	1268.750	Iinpk-			17.150
Pdc	*	*	1066.257	Pf	*	*	1.000
Eff	*	80.000	84.040	Vin	264.000	90.000	112.312

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	19.270	*	*	19.259	*	0.006
2.	0.010	20.550	*	*	20.550	*	0.007
3.	0.010	11.130	*	*	11.128	*	0.007
4.	0.010	12.000	*	*	12.003	*	0.005
5.	0.010	6.850	*	*	6.850	*	0.008
6.	0.010	6.000	*	*	5.995	*	0.006
7.	0.010	6.700	*	*	6.698	*	0.011
8.	0.010	7.000	*	*	7.000	*	0.011
9.	0.010	12.660	*	*	12.718	*	0.015
10.	0.010	12.000	*	*	11.990	*	0.013
11.	0.010	3.080	*	*	3.081	*	0.002

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.055	0.050	*	0.043
2.	3.465	3.135	3.256	0.120	*	0.050
3.	12.600	11.400	11.918	0.120	*	0.050
4.	12.600	11.400	11.881	0.120	*	0.075
5.	12.600	11.400	11.881	0.120	*	0.075
6.	12.600	11.400	11.881	0.120	*	0.100
7.	12.600	11.400	11.918	0.120	*	0.100
8.	12.600	11.400	11.906	0.120	*	0.100
9.	12.600	11.400	11.975	0.120	*	0.100
10.	12.600	11.400	11.981	0.120	*	0.088

11.	5.250	4.750	4.880	0.050	*	0.046

SEQ.10: Input/Output Test (100% LOAD) ---------- PASS

Vin = 118.000 Fin = 60.0 Delay Time = 1.000 Meas. Time = 1.000

Load	Name	MODE	Ifs	Vfs	Meas.	BITS	Vdc Filter	Noise Filter	Von
1.	5V	I	50	10	UUT	0000	300	20M	3.000
2.	3.3V	I	50	10	UUT	0000	300	20M	2.000
3.	12V3	I	50	100	UUT	0000	300	20M	8.000
4.	12V3	I	50	100	UUT	0000	300	20M	8.000
5.	12V2	I	50	100	UUT	0000	300	20M	8.000
6.	12V2	I	50	100	UUT	0000	300	20M	8.000
7.	12V1	I	50	100	UUT	0000	300	20M	8.000
8.	12V1	I	50	100	UUT	0000	300	20M	8.000
9.	12V4	I	50	100	UUT	0000	300	20M	8.000
10.	12V4	I	50	100	UUT	0000	300	20M	8.000
11.	5VSB	I	50	10	UUT	0000	300	20M	3.000

	Max	Min	Reading		Max	Min	Reading
Iinrms	*	*	12.550	Iinpk+	*	*	19.015
Pin	*	*	1419.250	Iinpk-			19.125
Pdc	*	*	1183.613	Pf	*	*	1.000
Eff	*	80.000	83.397	Vin	264.000	90.000	112.912

Ld	RISE	I/R	Idc Max	Idc Min	Idc Read	Vn Max	Vn Read
1.	0.100	21.410	*	*	21.396	*	0.007
2.	0.010	22.830	*	*	22.828	*	0.008
3.	0.010	12.700	*	*	12.718	*	0.007
4.	0.010	13.000	*	*	13.000	*	0.006
5.	0.010	7.280	*	*	7.271	*	0.008
6.	0.010	7.000	*	*	6.998	*	0.008
7.	0.010	7.220	*	*	7.220	*	0.012
8.	0.010	8.000	*	*	8.002	*	0.013
9.	0.010	13.400	*	*	13.462	*	0.016
10.	0.010	14.000	*	*	13.987	*	0.014
11.	0.010	3.420	*	*	3.412	*	0.003

Ld	Vdc Max	Vdc Min	Vdc Read	Vpp Max	Vpp Min	Vpp Read
1.	5.250	4.750	5.047	0.050	*	0.047
2.	3.465	3.135	3.249	0.120	*	0.055
3.	12.600	11.400	11.912	0.120	*	0.025
4.	12.600	11.400	11.868	0.120	*	0.075
5.	12.600	11.400	11.868	0.120	*	0.075
6.	12.600	11.400	11.868	0.120	*	0.075
7.	12.600	11.400	11.912	0.120	*	0.100
8.	12.600	11.400	11.893	0.120	*	0.101
9.	12.600	11.400	11.968	0.120	*	0.100
10.	12.600	11.400	11.975	0.120	*	0.096
11.	5.250	4.750	4.853	0.050	*	0.031

SEQ.11: Set Up Function () ---------- PASS

Vin_Port (1-4) = 1 Vin type = AC
ON Phase (mS) = 0.000 OFF Phase = 0.000
Setup off(Sec) = 0.100 PAUSE function= N
Display Message= PLEASE PRESS

Relay status: Pre value-1 = 00 Pre value-2 = 00
TTL status : Pre value-1 = 0000 Pre value-2 = 0000

Change state delay: For relay = 0 For TTL = 0
Ext. device GPIB address = * GPIB EOS byte = 1
Message for ext. device = V10

Load	Name	MODE	Ifs	Vfs	Meas	BITS
1.	5V	I	50	10	UUT	0000
2.	3.3V	I	50	10	UUT	0000
3.	12V3	I	50	100	UUT	0000
4.	12V3	I	50	100	UUT	0000
5.	12V2	I	50	100	UUT	0000
6.	12V2	I	50	100	UUT	0000
7.	12V1	I	50	100	UUT	0000
8.	12V1	I	50	100	UUT	0000
9.	12V4	I	50	100	UUT	0000
10.	12V4	I	50	100	UUT	0000
11.	5VSB	I	50	10	UUT	0000
